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Attorney Docket No.: P-2448-US1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): AVNI, Dror et al. Examiner: Ho, H.
Serial No.: 09/730,586 Group Art Unit: 2818
Filed: December 7, 2000
Title: PROGRAMMING AND ERASING METHODS FOR AN NROM ARRAY

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AMENDMENT

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

This Amendment is filed in response to the Office Action dated June 6, 2002 issued by the United States Patent and Trademark Office in connection with the above-identified Application. A response to the June 6, 2002 Office Action was due September 6, 2002. Applicants are concurrently filing a petition for a Three-Month Extension of Time, with the associated fees. Accordingly, a response to the June 6, 2002 Office Action is due by December 6, 2002, and thus this Amendment is being timely filed.

Kindly amend the above-identified application as follows:

In the Claims:

Please amend the following claims to read as follows:

1. A method for programming a memory array, the method using programming pulses applied to either the drain or gate of one or more memory cells within said memory array, the method comprising:
- adapting the duration or the amplitude of said programming pulses as a function of the current state of said memory array.
2. A method according to claim 1 wherein said step of adapting includes:
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